



<b>Form 1449 (Modified)</b>  <b>Information Disclosure Statement By Applicant</b>  (Use Several Sheets if Necessary)	Atty Docket No. ALTRP098/A1185	Application No.: 10/693,546
	Applicant: John R. Chase Filing Date October 23, 2003	Group 2123 <b>2128</b>

**U.S. Patent Documents**

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
	A1						
	A2						
	A3						
	A4						
	A5						

**Foreign Patent or Published Foreign Patent Application**

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
	B1							
	B2							
	B3							
	B4							
	B5							

**Other Documents**

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
SL	C1	Hutton et al., "The Circuit Characterization and Generation Project at the University of Toronto", <a href="http://www.eecg.toronto.edu/~mdhutton/gen/">www.eecg.toronto.edu/~mdhutton/gen/</a> downloaded September 8, 2003
SL	C2	Synopsys Products & Solutions, Design Platforms, <a href="http://www.synopsys.com/products/products.html">www.synopsys.com/products/products.html</a> site modified August 1, 2003, downloaded September 8, 2003
	C3	
Examiner		Date Considered <b>3/8/06</b>

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.